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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/592,009	06/12/2000	Sherman Lee	M-8815 US	4198

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EXAMINER

HUYNH, KIM NGOC

ART UNIT	PAPER NUMBER
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2182

DATE MAILED: 06/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/592,009

Applicant(s)

LEE ET AL.

Examiner

Kim Huynh

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 5/11/05.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 June 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 and 3-14 are rejected under 35 U.S.C. 102(e) as being anticipated by over Born et al. (US 6,247,040).

Claims 1 and 8, 12, Born discloses a method and apparatus for performing context switching having a peripheral device 100, interfacing with a host 140 via an interface 150 (Fig. 1) wherein the peripheral device 100 having first and second context registers (Fig. 2, active 202 and inactive 200 context registers in buffer 107, col. 5, ll. 40-43 and col. 8, ll. 62 to l. 3) wherein the first and second registers are associated with an index value (**context ID** associated with each disk transfer command, col.13, ll. 18-26 and col. 14, ll. 33-43 or a priority value associated with the type of requests/command from the host to determine the switching of context data, abstract, last 8 lines and col. 3, ll. 13-30), setting the context index to the second value to perform context switching (context ID is used to determine if context switching is required, col.

13, ll. 12-26 or the type of command is interpreted by the host channel interface to determine the associated priority for perform context , col. 5, ll. 7-11, col. 9, ll. 3-10 and col. 12, ll. 8-25), accessing context data in the second register 200 of a peripheral device when a context index is set to the second index value (*context switching operation*), an a register access circuit (buffer manage 112) for accessing the first or second register based on the value/command provided by the host.

Claims 3-6, Born discloses accessing context data including receiving an address value identifies the address within the register (address counters pointers provided by the host channel transfer or disk channel, col. 6, ll. 9-19), control input identifies read/write function (read/write command), and providing the content of the register (read command) or writing the data to the register (write command).

Claims 7 and 9, Born discloses the context registers are within the context management module (special purpose vs. architected registers).

Claims 10-11, the peripheral system includes an address, control, microprocessor and data portions (address pointer/counters, program memory 102, DMA 106, memory), the data portion includes the first and second registers (memory including buffers and registers).

Claim 13, Born discloses the plurality of context registers associated with a plurality of index values (each SCSI command includes a target device value and SCSI command descriptor block generated by the host command, col. 1. ll. 40-67 and is associated/ translated into physical storage/retrieval information by the disk controller).

Claim 14, since the index register of claim 14 is not limiting, it could be interpreted as any register associated with the memory system of the peripheral device.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 2 rejected under 35 U.S.C. 103(a) as being unpatentable over applicant Born in view of admitted prior art.

Born discloses the context data being device address on a network, and active member address (target being addressed) but does not disclose the context data being class value or clock offset values. As disclosed by applicant and well known in the art to utilize wireless communication among the electronic devices by utilizing Bluetooth communication to eliminate the need of buying or carrying cables for connecting between the devices. Bluetooth is aimed at simplifying communications and data synchronization among the electronic devices and includes clock offset and class value in its protocol. Born discloses implementing the context switching register with the peripheral device in order to maximize the use of resources within the target device and avoid loss of data and (col. 2, ll. 25-52). It would have been obvious to one having ordinary skill in the art to utilize Bluetooth communication in the system of Born in order to maximize resource utilization in a wireless environment.

Response to Arguments

Applicant's arguments filed 5/11/05 have been fully considered but they are not persuasive.

The examiner respectfully disagrees with the argument that Born does not disclose register access circuit access the first and second register based on a first or second index value provided by the host computer. Born discloses a **context ID** associated with each disk transfer command (col.13, ll. 18-26, col. 14 and ll. 33-43) or a **priority value associated with the type of requests/command** from the host to determine the switching of context data, abstract, last 8 lines and col. 3, ll. 13-30). Index value is a value serves as an indicator or a reference; in this case the context ID, or the priority associated with the command type, address and length value of the request can serve as an index value to determine the appropriate context switching operation.

Conclusion

THIS ACTION IS MADE FINAL. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim Huynh whose telephone number is (571) 272-4147.

6. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to be 'KH' with a stylized flourish extending to the right.

Kim Huynh
Primary Examiner
Art Unit 2182

KH
6/17/05